

Bruker BioSpin



Amplifier 700-900MHz INR Operating & Service Manual

Version 002

think forward

NMR Spectroscopy

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General Information

Introduction

The BLAH500 E Amplifier 700-900MHz INR is a broadband linear pulse power amplifier specifically designed for Nuclear Magnetic Resonance (NMR) application for 16,4 to 21,1 Teslas Systems. It is commercialized under the BRUKER BIOSPIN part number W1345505.

It is operated in AB linear class and provides 500W and more peak RF power over the frequency range 650-900MHz on the H500 output for the Solid applications and 50W and more peak RF power on the H50 output for the High Resolution applications.

The amplifier is equipped with **N-Channel RF LDMOSFETs** transistors of the latest generation. The unit can provide full power for any combination of pulse width and duty cycle up to 100ms and 20% in High Resolution mode and 10% in Solid mode.

Its built-in protection circuitry will allow lower power pulses for longer pulse widths and duty-cycles, maintaining a 50W average power for the Solid application on the H500 output and 10W average power for the High Resolution application on the H50 output.

The electronic protection circuitry has been designed to protect against:

- Excessive power output level (overdrive)
- Excessive pulse repetition rate (over duty-cycle protection)
- Excessive pulse duration (over pulse-width)
- More than 50% reflected RF power (mismatch when VSWR \geq 6)
- Thermal protection (overheat)

The amplifier is powered by an internal switched power supply assembly that provides the +32VDC for the power amplifiers, in addition to all low level voltages for the system.

The supply is self protected for overcurrent and overvoltage.

The entire unit is housed in a 19", 3U, 520mm rack cabinet.

General Information

This field indicates the variant number that identifies the production category of the product. The default variant is 00.

• (C) ECL

(B) Variant

This field indicates the revision number that identifies the product configuration. The initial revision is 0.00.

• (D) Serial Number

Α

Figure 2.1. Identifying Plate

• (A) Part Number

This field indicates the serial number of the product.

This field indicates the part number of the product.

/B/C/D

Е F

• (E) Type

This field contains the designation of the product.

• (F) Information This field contains additional information about the product.

may cause personal injury or damage to the equipment from misuse or abuse. Please read the labels and understand their meaning.

Identifying Plate

The BLAH500 E Amplifier 700-900MHz can be identified by an identifying plate at the front panel of the unit that contains the following information :

Labels are provided to alert operating and service personnel to conditions that

Identification Labels

The BLAH500 E Amplifier 700-900MHz is in accordance with the standard 61010-1 and with the UL 61010-1 / CSA C22.2 No.61010-1-04 Safety Requirements for Electrical Equipments.

Safety

7 (51)

2.1.1

2.1



Manufacturer's Name Plate

The BLAH500 E Amplifier 700-900MHz can be identified by a manufacturer's name plate at the back panel of the unit that contains the following information :

BRUKE	R Made i	n France
A VAC D KVA	B Hz E Amps	C Phase F Gnd
P/N : G		

Figure 2.2. Manufacturer's Name Plate

• (A) Voltage

This field indicates the input mains voltage of the product.

• (B) Frequency

This field indicates the input mains frequency of the product.

• (C) Phases

This field indicates the number of phases of the mains.

• (D) Power

This field indicates the absorbed power of the product.

• (E) Current

This field indicates the absorbed current of the product.

• (F) Wires

This field indicates number of wires with the ground in the mains cord.

• (G) Part Number

This field indicates the assembly number that identifies the part number of the product.

Safety	Labels	and S	ymbols
Salely	Labels	anu J	ymbols

2.2

2.2.1

Danger

DANGER! Risk of electrical shocks



Throughout this manual, this symbol indicates the possibility of severe personal injury, loss of life or equipment damage if the instructions are not followed.

On the equipment, the symbol also implies a danger and alerts the user.

Instruction

Operating personal should not remove RF output cable without turn off the power supply because the RF output can cause serious burns before the "Mismatch" protection is active.

Please disconnect the mains supply before opening to prevent potential hazard such as :

- Electrical shock from power supply
- Contact burns from the RF module and heatsink
- Finger scratch due to the fan assembly on the RF module.

Safety

Installation

The installation of the device must be done only by an authorized and qualified technician, in total accordance with the running standards.

BRUKER BIOSPIN assumes no liability for the customer's failure to comply with these requirements and is therefore not responsible or liable for any injury or damage that occurs as a consequence of non-approved installation.

Initial Inspection	3.1
Mechanical Check	3.1.1

If damage of the shipping cardboard is evident, request the carrier's agent to be present when the instrument is unpacked. Check the equipment for damage and inspect the cabinet and panel surfaces for dents and scratches.

Claim for Damage	3.1.2
Claim for Damage	3.1.2

If the unit is mechanically damaged or fails to meet specifications upon receipt, notify BRUKER or our representative immediately. Retain the shipping cardboard and packing material for the carriers inspection as well as for subsequent use in returning the unit if necessary.

Reshipment and Repackaging I	Requirements	3.1.3

Whenever possible, the original cardboard and packing material should be used for reshipment. If the original packing material is not available, wrap the instrument in heavy paper or plastic. Use a strong shipping container. If cardboard is used, it should be at least 200 lbs. test material.

Use shock absorbing material around all sides of the instrument to provide a firm cushion and to prevent movement from inside the container wall on each side. Protect the front panel by means of cardboard spacers inserted between the front panel and the shipping cardboard. Make sure that the instrument cannot move in the container during shipping. Seal the cardboard box with a good grade of shipping tape and mark the container :

"FRAGILE ELECTRONIC INSTRUMENT"

Environment Requirements

This amplifier is built for inside use only on a maximum elevation of 2000m above sea level (6600 feet).

No specific cooling or ventilation is required.

Be sure that the amplifier has enough area around it so that the free airs flow into and out of the amplifier is not obstructed.

It should, however, be in an environment which conforms to the 5° C - 45° C (41° F - 113° F) thermal specifications, a 80% maximum relative humidity of air and a contamination level of two (means a normal non-conductive contamination, temporary conductivity due to condensation is possible).

Installation Requirements				3.2				
						_		

No special precautions are necessary. Mount the equipment in an area which is relatively free of vibration, and has sufficient room for cable connections.

The amplifier has a class II installation category.

Bench Operation	3.2.1
-----------------	-------

The unit can be placed onto a secure flat surface.

Power Requirements

The BLAH500 E Amplifier 700-900MHz has a built-in switched power supply. The mains line connector on the rear panel is a CEI 10A.

One Phase Line requirements :

AC input voltage :	208-230VAC
Input current max :	3,4A
Frequency :	50/60Hz

System Check

Before applying power for the first time the following items should be checked:

- The AC input voltage 208-230 VAC ± 10% range must be compatible with the power supply.
- An external blanking (gating) pulse must be supplied to the amplifier in order for the unit to function. Ensure that this pulse has a proper level and logic polarity.
- The BLAH500 E Amplifier 700-900MHz has a nominal input level of +4dBm. Ensure that the system drivers are operating at these levels.
- Output RF loads are connected.

3.4

Initial Turn on Procedure

The following list describes how to turn on the BLAH500 E Amplifier 700-900MHz and what should be seen as this occurs.

Before starting this procedure, make sure that you have properly followed the instructions in section <u>"System Check" on page 12</u>.

- 1. Connect the amplifier to the AC line and turn the line switch to ON.
- Observe the indicators on the front panel of the amplifier : - The +32V ON LED's will illuminate,
 - The +15V, -15V and +3,3V ON LED's will illuminate.
- 3. System is now fully operational.

Operation

Front Panel Description

The BLAH500 E Amplifier 700-900MHz front panel is provided with 12 indicators for status monitoring, 7 RF connectors, 1 interface connector and 1 line switch.

Indicators

Normal operation is indicated when following LED's are ON.

+32V	Indicates that the +32V supply is applied.
+15V	Indicates that the +15V supply is applied.
-15V	Indicates that the -15V supply is applied.
+3,3V	Indicates that the +3,3V supply is applied.
Overdrive	Indicates when the peak power limit has been reached.
Duty Cycle (D.C.)	Indicates when the duty cycle limit has been reached.
Pulse Width (P.W.)	Indicates when the pulse width limit has been reached.
Mismatch	Indicates when the max. reflected power limit has been reached.
RF POW. FLT	Indicates when one of the above limits has been reached.
Overheat	Indicates that the thermistor located on the RF module heatsink has sensed excessive heatsink temperature. The amplifier is blanked until an accepable temperature is reached. The function is self-resetting and no maintenance is needed. Indicates also that a fan on the assembly stops turning. The amplifier is blanked until fans are changed.
H500 ON	Indicates when the RF Power is present on the Solid output.
H50 ON	Indicates when the RF Power is present on the High Resolution output.

Table 4.1. Indicators Assignment

4.1.1

Coaxial Connectors

	-
IN1, IN2, IN3	RF input of the embedded router, SMA type connector (female). Defaults entry is IN1 and allows to the BLAH500 E to deliver full power at nominal +4dBm drive.
H500	RF OUT H500 (Solid output), N type connector (female).
H50	RF OUT H50 (High Resolution output), N type connector (female).
BLNK	Blanking input, BNC type connector (female). TTL logic, 5V = blanking ON, 0V = blanking OFF. When BLANKING signal is at TTL level high (+5V), no gating is applied to the amplifier stages, and no RF Power is possible. When BLANKING signal is at TTL level low (0V), the amplifier stages are gated and RF Power is possible.
SEL H500/H50	Select input, BNC type connector (female). When the SEL H500/H50 signal is at TTL level low (0V), the Solid output H500 is selected. When the SEL H500/H50 signal is at TTL level high (+5V), the High Resolution output H50 is selected.

Table 4.2. Coaxial Connectors Assignment

Ethernet 10/100 Interface Connector

4.1.3

The RJ45 connector for the Ethernet 10/100 Mbps link is mounted directly on the BLA Control Board.

Pin 1	Transmit + (Tx+)
Pin 2	Transmit - (Tx-)
Pin 3	Receive + (Rx+)
Pin 4	N/A
Pin 5	N/A
Pin 6	Receive - (Rx-)
Pin 7	N/A
Pin 8	N/A

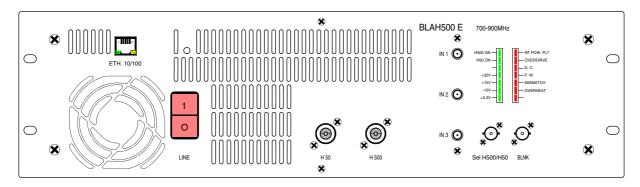


Figure 4.1. BLAH500 E Amplifier 700-900MHz Front Panel Design



Figure 4.2. BLAH500 E Amplifier 700-900MHz Front Panel View

Rear Panel Description

The BLAH500 E Amplifier 700-900MHz rear panel is free of elements in exception of the 3 poles (2P+E) line filter socket.



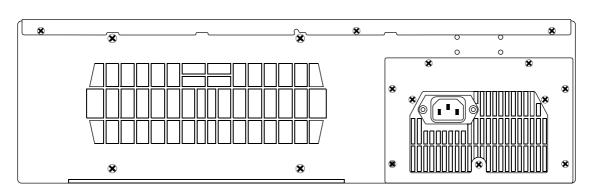


Figure 4.3. BLAH500 E Amplifier 700-900MHz Rear Panel Design



Figure 4.4. BLAH500 E Amplifier 700-900MHz Rear Panel View

4.2.1

Technical Description

System Overview

The BLAH500 E Amplifier 700-900MHz provides :

- A RF Output of 500W and more on the Solid Output H500, over the full frequency range 650 to 900MHz, when selected for Solid operation with SEL H500/H50 command controlled at TTL level low.
- A RF Output of 50W and more on the High Resolution Output H50, over the full frequency range 650 to 900MHz, when selected for High Resolution operation with SEL H500/H50 command controlled at TTL level High.

The RF section of the system consists of an embedded router fixed on the front panel and a linear module BLMH500/H50-E mounted around a single self-contained Push fan assembly heatsink.

The embedded router has three RF inputs and one RF output feeded to the driver amplifier located on the BLMH500/50-E module.

A linear class A / AB driver using switches and bias voltage gatings, delivers the RF input power to the Power Amplifier. This driver is located on the top of the heatsink assembly.

The class AB power amplifier is located on the bottom of the heatsink assembly.

The output of the power amplifier is connected to a bi-directional high dynamic coupler mounted on the front panel of the amplifier. This output will be the Solid H500 output when the amplifier is controlled for Solid applications.

When controlled for High Resolution applications, the output of the driver is switched to the front panel H50 output via a RF relay and a bi-directional high dynamic coupler.

The entire system is controlled by a Digital Signal Processing control board, processing information from the amplifier and blanking signal, providing protection from excessive peak power, duty cycle and pulse width for average power, maximum reflected power and heatsink over-temperature.

The DSP control board reads the indentification information of the amplifier (BIS).

Monitoring of fan status, supply status and LED's status is also performed by the control board.

Circuits such as BLAC6 Extension Board 1 Channel and Status LED's board, complete the amplifier assembly.

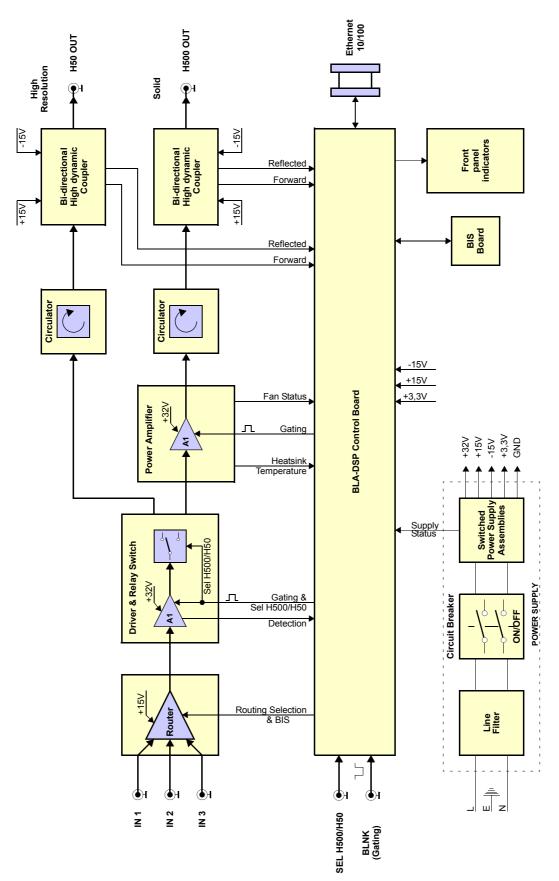


Figure 5.1. BLAH500 E Amplifier 700-900MHz System Block Diagram

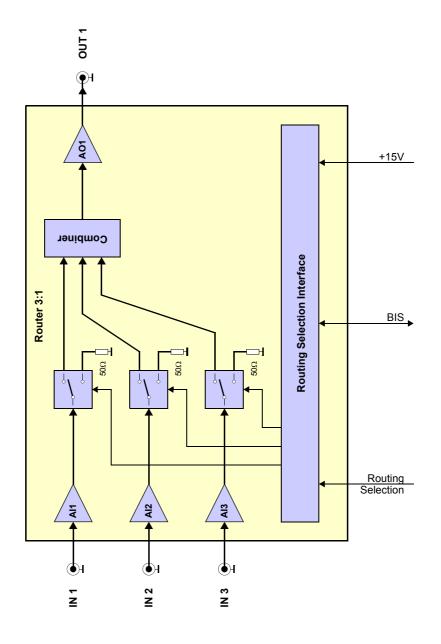


Figure 5.2. Embedded Router Block Diagram

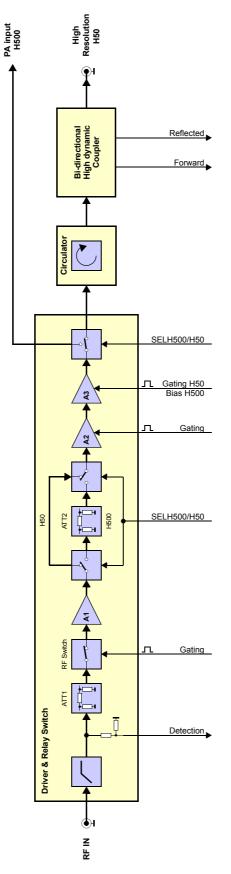


Figure 5.3. Driver & Relay Switch Block Diagram

Theory of Operation

RF Path

5.2.1

5.2

The BLAH500 E Amplifier 700-900MHz (P/N: W1345505) consists of a 3 input embedded router, a class A / AB driver amplifier and a class AB power amplifier.

A nominal input power level of +4dBm produces a rated linear output power of :

- 50W peak for 20% duty cycle at 100ms pulse width maximum on the High Resolution output H50 when selected as an High Resolution amplifier.
 In this case, the output of the 50W driver is directly switched to the front panel via a mechanical relay, a circulator and a bi-directional high dynamic coupler.
- 500W peak for 10% duty cycle at 100ms pulse width maximum on the Solid output H500 when selected as a Solid amplifier. In this case, the output of the 50W driver is switched to the input of the power amplifier via a mechanical relay.

The unit is also capable of longer pulses for lower average power, up to 50W CW power on the Solid output H500 and 10W CW power on the High Resolution output H50.

Embedded Router

The embedded router consists of a class A RF amplifier IC's and RF switches, manufactured on a Gallium Arsenide process.

It is built on a four independent cells architecture with three RF input cells and one output cell. The RF input cells ensure function of amplification and routing, the output cell ensures the functions of combining, RF amplitude thermo-stability and amplification.

The three RF inputs could be routed alone or combined each other to the RF output by selecting the wished RF path through the BLA controller board. Each entire RF path has a nominal 15dB of gain and operates at +15VDC.

Also, the router is equipped with an EEPROM for BIS information.

RF Driver

In the first section of the driver, the RF input signal is fed through a 5 poles high pass filter and the RF detection path. Then, via a thermal PAD attenuator and a GaAs RF switch, the RF signal is convoyed to a hybrid amplifier.

In this section, the GaAs RF switch requires a control board conditioned gating signal to control the operation of the switching element.

A commutable H500/H50 attenuator follows the hybrid amplifier.

This attenuator is built of two GaAs RF switches, commutated by a control board conditioned signal SEL H500/H50 and a thermo-compensated attenuator for compensation of the output power drift with temperature of the amplifier when the H500 output is selected.

This commutable attenuator is needed to minimize gain of about 5dB when the amplifier is operating in Solid application and provides full gain in High Resolution application. It is divided in variable resistive attenuator to adjust output power and the Thermal PAD.

The second section of the driver includes a class AB MOSFET transistor followed by a class AB LDMOSFET transistor.

The polarization of these transistors require a control board conditioned gating signal to control the bias voltage on the gate of the FET and depends of the selection H500/H50 to prevent the increasing of anti-droop behaviour. If the H500 output is selected, the gating signal is unblanked else if the H50 output is selected, it is blanked.

The circuitry around the transistors consists of complementary input and output transformers and baluns and operates the device in Push-Pull.

The RF driver has a nominal 25 to 30dB gain and operates at +32VDC.

With the embedded router gain, the entire path has a nominal 45dB gain in H50 mode for High Resolution operation and 40dB gain to drive the H500 power amplifier stage for Solid operation.

RF Relay H500/H50 Selection

The coaxial RF relay switches the RF power from the driver via a circulator and bi-directional high dynamic coupler to the High Resolution H50 output on the front panel, when the SEL H500/H50 signal is controlled to TTL level high or not connected.

When controlled by SEL H500/H50 signal at TTL level low, the relay switches the output of the driver to the power amplifier to built the Solid H500 output.

RF Coupler H50

The H50 bi-directional high dynamic coupler provides an approximate 1V peak DC signal for full 50W and also a peak DC signal for reflected power on the High Resolution H50 output.

Both signals, forward and reflected, are analyzed by the BLA control board for monitoring and protection setting on the H50 output.

RF Power Amplifier

The power amplifier includes four class AB LDMOSFET transistors mounted on a single flange. They are coupled with -6dB four ways splitter/combiner to built a nominal 16dB gain and operates at +32VDC. The power amplifier requires a control board conditioned gating signal to control the bias voltage on the gate of the FETs.

RF Coupler H500

The H500 bi-directional high dynamic coupler on the front panel provides an approximate 1V peak DC signal for full 500W and also a peak DC signal for refleted power on the Solid H500 output.

Both signals, forward and reflected, are analyzed by the BLA Control board for monitoring and protection setting on the H500 output.

Circulator

The RF circulator is a three-port passive device made of magnets and ferrite material used to control the direction of signal flow in a circuit. In our case, the circulator is used as an isolator to protect the output power transistor from excessive signal reflection. There is a circulator incorporated at the end of the power amplifier, just before the high dynamic coupler.

BLA Control Board

The BLA Control Board has 3 main functions:

- 1. Conditions the input blanking (BLNK) signal and delivers it to the above mentioned RF Paths.
- 2. Allows Ethernet communication with the workstation.
- Monitor the output characteristics of the amplifier thanks to the DC peak detection of the bi-directional coupler. Electronic circuitry processes the detection information and protect the amplifier from overstress like :
- Forward and reflected peak power

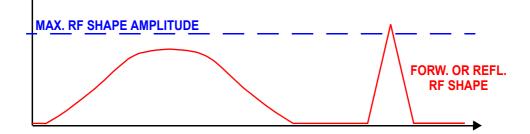


Figure 5.4. Peak Power Limitation

The peak power limitation is the maximum RF forward shape amplitude allowed at the amplifier output.

Limitation range : from 1% to 200% of nominal power.

The peak power limitation is checked for each sample (10 million samples per second), and the maximum peak value is latched then cleared by a read operation (for monitoring purpose).

· Forward pulse width

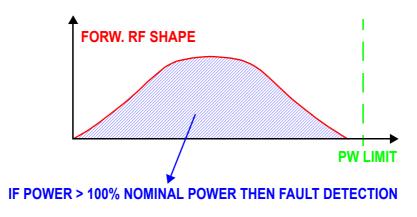


Figure 5.5. Forward Pulse Width Limitation

The pulse width is the lapse of time during which the nominal power can be applied.

Limitation range : from 0.1ms to 512ms.

The pulse width value is updated every 100µs.

• Forward pulse duty cycle

The duty cycle value is the ratio between measured input power during pulse width limitation value divided by duty cycle limitation value and the nominal power during the same time.

For example, if the pulse width limitation is set to 3ms and duty cycle is limited to 10%, then duty cycle value equals the measured input power during 30ms (3ms / 0.1) divided by the nominal power during 30ms.

Limitation range : from 1% to 100%.

The duty cycle value is updated every 100µs.

Excess of reflected power (Mismatch)

The mismatch value is the ratio between the reflected power value and the forward power value.

Limitation range : from 1% to 100%.

The mismatch value is updated every 100µs.

Other protections

The control board also detects the following faults :

- Power supply fault
- Fan failure
- Heat sink temperature to protect against thermal overstress
- Fault detection timings

Table 5.1. Fault Detection Timings

Fault	Detection delay (max)
Peak power	500 ns
Duty cycle	100 µs
Pulse Width	100 µs
Mismatch	100 µs
Power Supply, Fan	200 ns
Heat sink temperature	500 ms

Peak, pulse width, duty cycle, mismatch and also mean power values can by read out at any time from the main DSP for monitoring purpose.

- Fault protection reset.

If one of these overstresses appears the board automatically resets the fault flags after 2 seconds, the gating signal is disabled and the status led board on the front panel displays the fault.

This means, for example, that when a pulse width fault occurs, the amplifier channel is disabled after the detection delay. The side effect is that the fault condition disappears since the channel's output power is null.

After 2 seconds, the channel is switched on and the cycle begins again (unless the channel RF input signal is re-adjusted to meet the power limitations).

BLA Extension Board

This board gives the information to the control board of RF detection.

Status Led Board

The status led board, on the front panel of the amplifier, displays overstress functions, supplies status, and so on, as described in <u>"Indicators" on page 15</u> and <u>"BLA Control Board" on page 25</u>.

BIS Board

The universal BIS board is located on the amplifier case and contains identifications of the amplifier.



Technical help : please contact your local representative.

5.2.5

5.2.3

5.2.4

Technical Description

Servicing the BLA

6.1

The BLAH500 E Amplifier 700-900MHz provides diagnosis and servicing web pages relies on HTTP, allowing service access with any web browser.

Accessing the BLA Amplifier

The BLAH500 E Amplifier 700-900MHz is accessible via the BLA control board with its IP address.

The IP address is given during $\ensuremath{\text{"cf}}\xspace$ by using TOPSPIN 2.xx software on the workstation.

In case of problems :

- Check the RJ45 cabling between amplifier, Ethernet switch and workstation.
- Check the Ethernet switch power.
- Check if the green LED on the amplifier RJ45 connector lights up.
- Check the front panel of the amplifier, LED's indicators +32V, +15V, -15V and +3.3V ON must have lit.

To access the BLAH500 E Amplifier 700-900MHz, type "**ha**" in TOPSPIN 2.xx or better and choose the BLA that should be accessed or start your favourite web browser and type the given IP address as URL.

Sub Toolbar Information

Device Information (default)

You should get the following start screen.

	BLA							
	Information Ac	Ivanced Operations	Maintenance					
	Device Informati	on						
formation	·							
mplifier information	Name:	BLAH500 E 700-900MHZ	INR					
mplifier status	Part number:	W1345505						
inpliner actua	Serial number:	0001						
	Ecl:	0						
	Manufacturing location:	BFR						
	Manufacturing date:	8/19/09						
	BIS type:	BLA						
	Software Versions							
	Boot version: Kernel version:	20051018 Windows CE 5.0						
	Application version:	BLAC6_20090311						
	Application version:	BLAC6_20090311						
	Channel Information	1						
				Limit	s			
	Ch. Type Nom. pow	er Min freq. Max f	req. Duty cycle	Pulse width	Mismatch	Forw. peak	Output num.	Input num.
	1 H 500	W 650 MHz 900	MHz 10 %	100 ms	250 W	1000 W	1	Via router
nk forward	1 11 000		10.10	100 110	(50 %)	(200 %)	-	ind routon
		ion Information						
	High Resolution Opt							
		Limi Duty cycle Pulse width		w. peak Outp	ut num.			

6.2

6.2.1

© 2005-2008 Bruker Biospin SA. All rights reserved. Figure 6.1. Device Information

This page gives you general information about the amplifier (default page).

In the main toolbar, we can see that a BLA is displayed.

The left panel is the navigation menu. It can be used to navigate through the service pages or choose another tab in the sub toolbar.

Amplifier Status

6.2.2

Bruker Linear Amplif	fiers			BRUKEP
	BLA			
	Information	Advanced Operations	Maintenance	
	Amplifier status			
Information	-			
Amplifier information	Name:	BLAH500 E 700-900MHZ	INR	
Amplifier status	Part number:	W1345505		
	Serial number: Ecl:	0001		
	Eci:	U		
		Channel 1 (HR		
	Type:	H	2	
	Nominal power:	50 W		
	Forward peak power:	0.00 %		
	Forward average powe			
	Reflected power:	0 %		
	Overdrive:	ок		
	Pulse width:	ок		
	Duty cycle:	OK		
1997 - 1997 -	Mismatch:	ОК		
hink forward	General gating fault:	ок		
nink forward	Supply:	ок		
	Thermal:	ок		
	Blanking signal state:	inactive		
	Input power:	none		
	Output power:	none		
	High resolution state:	active		
	Switchbox position:	0xFF		

Leads you to a page giving information about the current status of the amplifier.

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Figure 6.2. Amplifier Status (High Resolution)

	BLA	k ter severe sever		
	Information	Advanced Operations	Maintenance	
	Amplifier status			
Information				
Amplifier information	Name:	BLAH500 E 700-900MHZ	INR	
Amplifier status	Part number:	W1345505		
	Serial number:	0001		
	Ecl:	0		
		Channel 1		
	Туре:	н		
	Nominal power:	500 W		
	Forward peak power:	0.00 %		
	Forward average powe	er: 0.00 %		
	Reflected power:	0 %		
	Overdrive:	ок		
	Pulse width:	ОК		
	Duty cycle:	ок		
	Mismatch:	ок		
	General gating fault:	ОК		
hink forward	Supply:	ОК		
	Thermal:	ОК		
	Blanking signal state:	inactive		
	Input power:	none		
	Output power:	none		
	High resolution state:	inactive		
	Switchbox position:	0×FF		

Figure 6.3. Amplifier Status (Solid)

Sub Toolbar Advanced Operations

Device Information (advanced)

You should get the following start screen.

	BLA												
	In	formatio	on A	dvanced O	perations	Ma	intenance						
	Dev	rice In	format	ion									
dvanced Operations										_			
Amplifier limitations	Nam	e:		BLAH500	E 700-900M	HZ INR							
	Part	number:		W134550	5								
Routing information &	Seria	al numbe	er:	0001									
etting	Ecl:			0									
	Manu	afacturing	g location:	BFR									
	Manu	facturing	g date:	8/19/09									
	BIS I	type:		BLA									
	Soft	ware V	/ersions							_			
		version:		20051018									
		el versio		Windows									
	Appli	ication ve	ersion:	BLAC6_2	0090311								
	- 1												
	Cha	nnel Inf	formatio	n		1							
	Ch	Туре	Nom. por	wor Min	freq. Ma	ax freq.	Duty cyc	le Pulse v	Limits	match	Forw. peak	Output num.	Input num
										250 W	1000 W	1.7	2.0 0
nk forward	1	н	500	0 W 650	MHz 9	00 MHz	10	% 10		(50 %)	(200 %)	1	Via router
IN IUI Walu													
	High	Resolu	ution Op	tion Inform	nation								
					L	imits							
							Concept and and	Forw. peak	Output num				
	Ch.	Nom.	power	Duty cycle	Pulse widt	h N	lismatch	Forw, peak	Output nun	5 M			

6.3

6.3.1

© 2005-2008 Bruker Biospin SA. All rights reserved. Figure 6.4. Device Information

This page gives you general information about the amplifier.

The left panel is the navigation menu. It can be used to navigate through the service pages or choose another tab in the sub toolbar.

Amplifier Limitations

Leads you to a page giving several default and current limits of the amplifier.

If you want, for any reasons, to change the current limits of the amplifier, press *Change limits*.

	Information	Advanced Op		Maintenanc	_				
	Information	Advanced Up	erations	maintenanc	e				
200 200	Amplifier limita	tions							
anced Operations	Name:	DI AUGODIO	700-900MHZ	IND			1		
mplifier limitations	Part number:	W1345505		. INK					
outing information &	Serial number:	0001							
tting	Ecl:	0							
	High Resolution (Ontion Inform	ation						
	Channel 1 (HR)		adon		Limi	ts			
	Type Nom. power	Min freq.	Max freq.	Duty cycle	Pulse width	Mismatch	Forw. peak	Output num.	Input num.
	H 50 W	650 MHz	900 MHz	20 %	100 ms	25 W (50 %)	100 W (200 %)	2	Via router
	Current limits :			20 %	100 ms	25 W	100 W	Chang	e limits
nk forward									

Figure 6.5. Amplifier Limitations (High Resolution)

	Inform	ier limita	Advanced Ope		Maintenance					
dvanced Operations	Name: Part num			700-900MHZ 1	INR					
Routing information & setting	Serial nu Ecl:		0001							
	Channe	el Informat	on			Limi	te	1		
	Туре	Nom. power	Min freq.	Max freq.	Duty cycle	Pulse width	Mismatch	Forw. peak	Output num.	Input num.
	н	500 W		900 MHz	10 %	100 ms	250 W (50 %)	1000 W (200 %)	1	Via router
	Current	limits :			10 %	100 ms	250 W	1000 W	Chang	e limits
nk forward										

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Figure 6.6. Amplifier Limitations (Solid)

Change Limits

Read the warnings, change limit parameters and press *Apply* if you are sure of that.

BLA						
In	formation	Advanced Operat	ions M	aintenan	e	
Cha	annel limita	tions				
	e: number: al number:	BLAH500 E 70 W1345505 0001 0)-900MHZ INR	L		
High	Resolution	Option Limitation	S			
High	Changir Please u	Option Limitation g these settings will o se this page for test H	verwrite the I	imitations	lefined with th	e spectrometer
Тур	Changir Please u	g these settings will o se this page for test	verwrite the I	imitations	lefined with th	e spectrometer
Тур	Changir Please t	g these settings will o se this page for test H	verwrite the I	imitations New	lefined with th Maximu	
Type	Changir Please t	g these settings will o se this page for test H 50 W	verwrite the I ourposes only	New	Maximu	
Type Norr	Changir Please t e : ninal power :	g these settings will o se this page for test H 50 W Default	verwrite the I ourposes only Current	New	Maximu	n) 96
Type Norr Duty Puls	Changin Please t e : ininal power : v cycle limit :	g these settings will o see this page for test H 50 W Default 20 %	Current	New 20 9 100 r	Maximu • 2 •s 100	n) 96
Typy Nom Duty Puls Misr	Changir Please u inal power : v cycle limit : e width limit :	H So W Default 20 % 100 ms 25 % (50 %)	Current 20 % 100 ms	New 20 9 100 r 25 V	Maximu , 2 is 100	n) % ms
Typy Nom Duty Puls Misr	Changir Please t inial power : v cycle limit : e width limit : natch limit :	H So W Default 20 % 100 ms 25 W (50 %) 100 ms	Current 20 % 100 ms 25 W	New 20 9 100 r 25 V	Maximu , 2 is 100	n 96 ms 0 W
Type Norr Duty Puls Forv	Changir Please t inial power : v cycle limit : e width limit : natch limit :	H So W Default 20 % 100 ms 25 W (50 %) 100 ms	Current 20 % 100 ms 25 W	New 20 9 100 r 25 V 100 V	Maximu , 2 is 100	n 96 ms 0 W

Figure 6.7. Change Limits (High Resolution)

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	BLA								
	Information	Advanced Operatio	ons M	laintenance					
	Channel limitat	tions							
Advanced Operations									
Amplifier limitations	Name:	BLAH500 E 700-	900MHZ INF	2					
	Part number:	W1345505							
Routing information &	Serial number:	0001							
setting	Ecl:	0							
	Changing Please us) these settings will ov se this page for test pu H	erwrite the I urposes only	limitations define	d with the spec	rometer so	oftware.		
	Type :	e this page for test pu	erwrite the I irposes only	limitations define	d with the spec	rometer so	oftware.		
	Please us Type : Nominal power :	H 500 W	erwrite the I Irposes only	limitations define	d with the spec	trometer so	oftware.		
	Type : Nominal power : Min. frequency :	H 500 W 650 MHz	erwrite the l Irposes only	limitations define	d with the spec	trometer si	oftware.		
	Please us Type : Nominal power :	H 500 W	erwrite the l irposes only Current	limitations define	d with the spec	trometer si	oftware.		
	Type : Nominal power : Min. frequency :	H 500 W 650 MHz 900 MHz	irposes only	New		trometer si	oftware.		
nink forward	Please us Type : Nominal power : Min. frequency : Max. frequency :	H 500 W 650 MHz 900 MHz Default	Current	New	Maximum	trometer si	oftware.		
nink forward	Type : Nominal power : Min. frequency : Max. frequency : Duty cycle limit :	H 500 W 650 MHz 900 MHz Default 10 %	Current	New 10 % 100 ms	Maximum 10 %	trometer si	oftware.		
nink forward	Please us Type : Nominal power : Min. frequency : Max. frequency : Duty cycle limit : Pulse width limit :	H 500 W 650 MHz 900 MHz Default 10 % 100 ms 250 W	Current 10 %	New 10 % 100 ms 250 W 1000 W	Maximum 10 % 100 ms	trometer si	oftware.		
nink forward	Please us Type : Nominal power : Min. frequency : Max. frequency : Duty cycle limit : Pulse width limit : Mismatch limit :	H H 500 W 550 MHz 900 MHz Default 10 % 100 ms 250 W (50 %) 1000 W	Current 10 % 250 W	New 10 % 100 ms 250 W	Maximum 10 % 100 ms 500 W	trometer si	oftware.		
nink forward	Please us Type : Nominal power : Min. frequency : Max. frequency : Duty cycle limit : Pulse width limit : Mismatch limit :	H H 500 W 550 MHz 900 MHz Default 10 % 100 ms 250 W (50 %) 1000 W	Current 10 % 250 W	New 10 % 100 ms 250 W 1000 W	Maximum 10 % 100 ms 500 W	trometer, si	oftware.		

Figure 6.8. Change Limits (Solid)

Routing Information and Setting

6.3.4

Leads you to a page giving information about the current routed RF path at the amplifier inputs.

Default RF path is INPUT 1 to CHANNEL 1.

	BLA	Advanced 0	perations	Maintenance	
	Routing Info	rmation & S	etting		
nced Operations	Name:	01.01/500	E 700-900MHZ I		
olifier limitations	Part number:	W134550		NK.	
ting information &	Serial number:	0001			
ing	Ecl:	0			
	Input routing	use this page for	test purposes on		
	Input routing	use this page for Current channel	Possible	New route	
	Input routing	use this page for Current	Possible	ly.	
	Input routing	use this page for Current channel	Possible	New route	
< forward	Please	use this page for Current channel	Possible channels	New route Channel 1 v set new route	
< forward	Please Input routing Input number 1 2	Use this page for Current channel 1 -	Possible channels 1	New route Channel 1 v Set new route no route v Set new route	
< forward	Input routing Input routing Input aumber I I I I I I I I I I I I I I I I I I I	Use this page for Current channel 1 -	Possible channels 1	New route Channel 1 v Set new route no route v Set new route	
< forward	Input routing Input number 1 2 3 Output routing	Current Channel 1 - Current	Possible channels 1 1 1 Possible	New route Channel 1 v set new route no route v set new route no route v set new route	

Figure 6.9. Routing Information and Setting

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Read the warnings, it is allowed to change routing configuration of the input router (ex: new route INPUT 2 to CHANNEL 1), press *set new route* if you are sure of that.

Sub Toolbar Maintenance

Device Information (maintenance)

You should get the following start screen.

2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2		BLA								
Antenance Self test & Software reset Firmware update BIS content BIS content Name: BLAH500 E 700-900MHZ INR Part number: W1345505 Serial number: 0001 Ed: 0 Manufacturing location: BFR Manufacturing location: BLACE_20090311 Channel Information Limits High Resolution Option Information Limits		Information	Advanced Oper	ations M	laintenance					
Self test & Software reset Name: BLAH500 E 700-900MHZ INR Part number: 001 Erin number: 001 Edition 0 BIS content 0 BIS content 0 Software Version: 2050118 Kernel Version: 2050118 Kernel Version: 2050118 Kernel Version: BLACE_20090311 Channel Information Limits Ch. Type Nom. power Min free. Max free. 1 H 500 W 650 MHz 900 MHz High Resolution Option Information Limits 1	۵۰ ۱	Device Informa	ation							
nk forward Channel Information Channel Information El. 2005 MHz Image: Software reside El.	intenance									
Serial number: 0001 Ed: 0 Manufacturing location: BFR Manufacturing date: 8/19/09 BIS type: BLA Software Version: 20051018 Kernel version: 20051018 Kernel version: 2005018 Channel Information Limits Channel Information Limits It H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via to the top				00-900MHZ INF	2					
Ed: 0 IS content BrR Manufacturing location: BFR Manufacturing date: 3/19/09 BIS type: BLA Software Version: 20051018 Kernel version: 20051018 Kernel version: BLACE_20090311 Channel Information Limits Channel Information 1 H 500 W 650 MHz 900 MHz 100 ms 250 W 1000 W 1 Via r High Resolution Option Information Limits Limits Limits Limits Limits	oftware reset									
IS content Manufacturing location: BFR Manufacturing date: 8/19/09 BIS type: BIS type: BLA Software Version: 20051018 Kernel version: Manufacturing ioaction: 20051018 Kernel version: Channel Information Limits Ch. Type Non. power Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. I H S00 W 650 MHz 900 MHz 10 % 100 ms 250 W 100 ms 250 W 100 ms 100 ms			Serial number: 0001							
13 content Manufacturing date: 8/19/09 B15 type: BLA Software Version: BLA Software Version: 20051018 Kernel version: Windows CE 5.0 Application version: BLACE_20090311 Channel Information Limits In H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via the second of the secon	rmware update									
Instruction of type: BLA Software Versions Boot version: Boot version: 20051018 Kernel version: Windows CE 5.0 Application version: BLAC6_20090311 Channel Information Limits										
Limits Software Versions Boot version: 20051018 Kemel version: Windows CE 5.0 Application version: BLAC6_20090311 Channel Information Limits Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. Input 1 H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via the forward High Resolution Option Information Limits Limits Limits	S content	Manufacturing date:	8/19/09							
Boot version: 20051018 Kernel version: Windows CE 5.0 Application version: BLACG_20090311 Channel Information Limits Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. Input 1 H S00 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via r High Resolution Option Information Limits Limits Limits Limits Limits		BIS type:	BLA							
Boot version: 20051018 Windows CE 5.0 Application version: BLAC6_20090311 Channel Information Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. I H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 w 1 H S00 W 650 MHz 900 MHz 100 ms 250 W 1000 w 1 w <th></th>										
Kernel version: Windows CE 5.0 BLACE_20090311 Channel Information Limits Ch. Type Nom, power Min freq. Max freq. Duty cycle Pulse width Min med. Forw.peak 0 W 650 MHz 900 MHz 1 H 500 W 500 W 650 MHz 10 % 100 ms 250 W 1000 W 1 H 500 W 1 Units		Software Version	S							
Application version: BLAC6_20090311 Channel Information Limits Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. 1 H 500 W 50 MHz 900 MHz 10 % 100 ms 250 W 1000 ws 1 H 500 W 1 H 500 W 1 H 500 W 1 H 500 W 100 ms 250 W 10 ms 10 ms		Boot version:	20051018							
Channel Information Channel Information Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. Input 1 H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via r High Resolution Option Information Limits		Kernel version:	Windows CE	5.0						
hk forward High Resolution Option Information		Application version:	BLAC6_2009	0311						
Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. Input 1 H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 100 V										
Ch. Type Nom. power Min freq. Max freq. Duty cycle Pulse width Mismatch Forw. peak Output num. Input 1 H S00 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via t High Resolution Option Information Limits		Channel Informat	tion							
1 H 500 W 650 MHz 900 MHz 10 % 100 ms 250 W 1000 W 1 Via r High Resolution Option Information						Lim	its			
Ik forward Image: Note of the second secon		Ch. Type Nom.	power Min fre	q. Max freq	. Duty cycle	Pulse width	Mismatch	Forw. peak	Output num.	Input nur
Ik forward High Resolution Option Information Limits		1 H 4	500 W 650 MH	17 000 MH	10.%	100 mc			1	Via route
High Resolution Option Information	k forward	1 11 1	000 11	12 500 Mills	10 /0	100 113	(50 %)	(200 %)	1	Via route
Limits	ik ioi wara									
		High Resolution C	Option Informa	tion						
Ch. Nom, power Duty cycle Pulse width Mismatch Forw. peak Output num.										
				Limits						
1 50 W 20 % 100 ms 25 W 100 W 2 (50 %) (200 %) 2		Ch. Nom. power	Duty cycle F		Mismatch Fo	rw. peak Out	put num.			

6.4

6.4.1

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This page gives you general information about the amplifier.

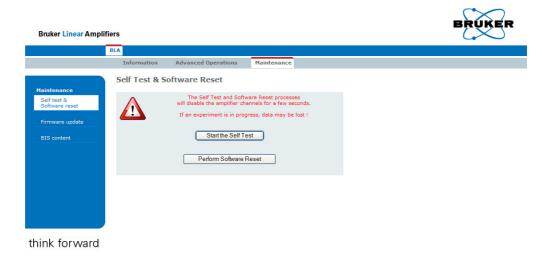
The left panel is the navigation menu. It can be used to navigate through the service pages or choose another tab in the sub toolbar.

Self-Test & Software Reset

6.4.2

Leads you to a page allowing you to do a self-test on the BLA control board (Hardware test) and to do a software reset.

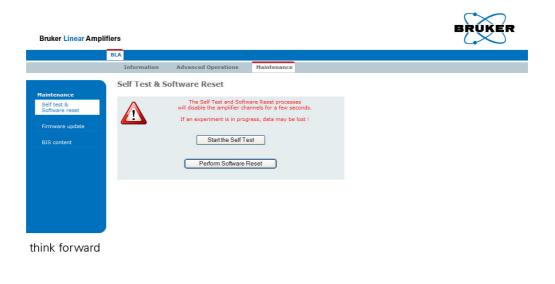
Both operations can be done if the amplifier doesn't work correctly.



	BLA				
	Inform	ation	Advanced Operations	Maintenance	
	Self te	st			
Maintenance					1
Self test &	Name:		BLAH500 E 700-900MHZ	INR	
Software reset	Part num		W1345505		
Firmware update	Serial nur Ecl:	mber:	0001		
Timware update	ECI;		0		1
BIS content	Line	Severi	ty Message		
	000001	INFO	Log started		
	000002	INFO	Memory load: 24%		
	000003	INFO	Notification message pump i	is OK and has number 0x000400d0	
	000004	INFO	Core application start event	handle is OK and has number 0x01ea88ca	
	000005	INFO	Core application control obj	ect is OK and has number 0x00040100	
	000006	INFO	Peripheral driver handle is 0	OK and has number 0xa1ea488a	
	000007	INFO	Serial driver handle is OK a	nd has number 0x61ea4d52	
	800000	INFO	Blanking mask object is OK	and has number 0x000412c0	
	000009	INFO	Enable blanking mask during	g initialization	
	000010	INFO	Successfully allocated memory	ory for BIS list	
hink forward	000011	INFO	Controller board BIS: I2C d	evice is present	
unit tor ward	000012	INFO	Controller board BIS: plugg	ed device into I2C driver	
	000013	INFO	Controller board BIS: read	data from I2C device	
	000014	INFO	Controller board BIS: unplu	gged device from I2C driver	
	000015	INFO	Controller board BIS: data :	size is 138 bytes	
	000016	INFO	Controller board BIS: data v		
	000017	INFO	Housing BIS: I2C device is		
	000018	INFO	Housing BIS: plugged device		
	000019	INFO	Housing BIS: unplugged dev	vice from I2C driver	
	000020	INFO	Housing BIS: data size is 20	0 bytes	
	000021	INFO	Housing BIS: data written to	output file	

Figure 6.11. Perform Self Test and Report

Read the warnings, press *Start the Self Test*. You should have only gray lines in the report.



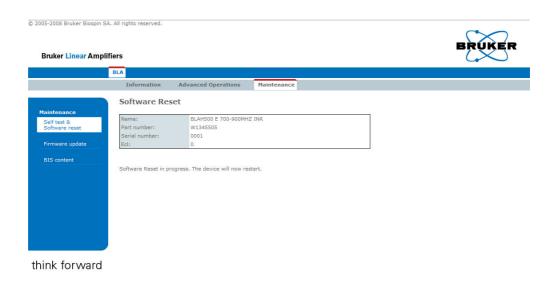


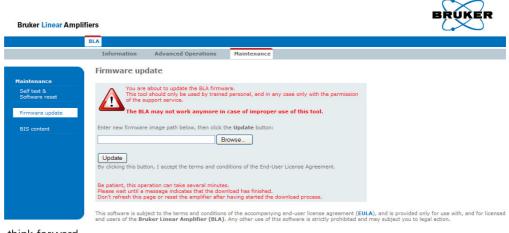
Figure 6.12. Perform Software Reset and Report

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Read the warnings, press *Perform Software Reset*. You should have the following screen.

Firmware Update

Leads you to a page allowing you to download new firmware.



think forward

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Figure 6.13. Firmware Update

Read the warnings, press the **Browse** button for selecting the new firmware file to download and press **Update**. Download the new firmware will take a few minutes.



NOTE : This button caption depends on your operating system language settings.

BIS Content

Leads you to a page giving information about the current BIS programmed on the amplifier.

	BLA			
	Information	Advanced Operations	Maintenance	
	BIS Content			
intenance				
elf test &	BIS Id:	1		
oftware reset	BIS description:	BLA Controller		
	BIS type:	BLAC		
rmware update	BIS content:	\$Bis,1,20071105,2048,8 \$Prd,W1522050,4526,2,		
IS content		\$Nam,BLA CONTROL BO		
13 content		\$CtrlVers,1.0,6.0# \$EndBis,f6, f2#		
	BIS Id:	2		
	BIS description:	BLA Housing		
	BIS type:	BLA		
	BIS content:	\$Bis,1,20071115,2048,E		
		\$Prd,W1345505,0005,0, \$Nam,BLAH500 E 700-9		
		\$Amp,1.1,1,H,,500,F8,1	0,100,650,900,1,0,50,200,200,#	
		\$HROpt,1.0,1,,50,20,10 \$EndBis,49, 44#	D,650,900,2,50,200,200#	
	BIS Id:	3		
nk forward	BIS description:	Mini-Router		
	BIS type:	BLAROUTER		
	BIS content:	\$Bis,1,20071115,2048,8		
		\$Prd,W1345212,0396,0, \$Nam,BLMMR31 RF AMP		
		\$RtInChar, 1.0, 10, 1000,	15#	
		\$RtInPath,1.0,1,1,1# \$RtInPath,1.0,2,1,#		
		\$RtInPath,1.0,3,1,#		

Figure 6.14. BIS Content

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Specifications

Common Characteristics

	Ampliner Common Characteristics
Constant Internal Protection	Supplies, fans faults and over temperature. Forward Power : peak & CW power, pulse width and duty cycle. Reflected Power : peak & CW power, self resetting protection shuts the amplifier off if the load VSWR is excessive.
Front Panel Indicators	Amplifier Status Led Board.
Front Panel Interfaces	1 x I/O 8 pins RJ45 connector.
Front Panel Controls	1 x AC line ON/OFF switch, 1 x SEL H500/H50 control signal.
Front Panel Connectors	3 x RF input, 2 x RF output, 1 x blanking input (gating).
Rear Panel Connectors	1 x AC line in socket.
Cooling System	Forced-air cooling (from front to rear).
Size	19" rack cabinet x 3U height x 520mm depth.
Weight	19kg
Power Requirements	208-230 VAC ± 10% single phase 50-60Hz. Bruker Biospin part number W1304006. Consumption max. 0.90kVA.

Table 7.1. Amplifier Common Characteristics

General Specifications

Channel High Resolution 50W Output

Table 7.2.	Channel High	Resolution	5011/	Output	Specifications
		Resolution	5000	Oulpul	Specifications

Frequency Range	650 to 900MHz
Linear Gain	45dB ±1dB typical
Gain Flatness	±1.5dB max.
Minimum Pulsed Output Power (@ nominal Input +4dBm)	50W min. full range
CW Output Power (Internal Limitation)	10W max.
Linear Output Power	35W typical @ 1dB compression
Linearity	±1dB to 35W typical
Amplifier Biasing	Class AB operation
Blanking Delay Time	1µs min.
RF Rise Time	< 100ns
RF Fall Time	< 50ns
DC Ringing	±100mV typical (due to blanking signal)
Input Noise Figure	8dB typical
Output Noise Power (Unblanked)	-119dBm @ 1Hz
Output Noise Power (Blanked)	Thermal Noise
Input/Output Impedance	50Ω
Input V.S.W.R. Route OFF	1.6 : 1 max.
Input V.S.W.R. Route ON	1.5 : 1 max.
Output Harmonics (2fc ; 3fc)	-45dBc ; -70dBc max. @ 50W
Pulse Width (Internal Limitation)	100ms @ 50W (up to CW @ 10W)
Duty Cycle (Internal Limitation)	20% @ 50W (up to 100% @ 10W)
Droop & Pulse Flatness	±2% typical @ 50W for 100ms Pulse Width
Amplitude Stability vs. Temperature	±0.1% / °C max.

7.2.1

Channel Solid 500W Output

7.2.2

Fraguanay Panga	650 to 900MHz
Frequency Range	
Linear Gain	56dB ±1dB typical
Gain Flatness	±2dB max.
Minimum Pulsed Output Power (@ nominal Input +4dBm)	500W min. full range
CW Output Power (Internal Limitation)	50W max.
Linear Output Power	550W typical @ 1dB compression
Linearity	2dB / -1dB to 550W typical
Amplifier Biasing	Class AB operation
Blanking Delay Time	1µs min.
RF Rise Time	< 100ns
RF Fall Time	< 50ns
DC Ringing	±100mV typical (due to blanking signal)
Input Noise Figure	8dB typical
Output Noise Power (Unblanked)	-108dBm @ 1Hz
Output Noise Power (Blanked)	Thermal Noise
Input/Output Impedance	50Ω
Input V.S.W.R. Route OFF	1.6 : 1 max.
Input V.S.W.R. Route ON	1.5 : 1 max.
Output Harmonics (2fc ; 3fc)	-40dBc ; -65dBc max. @ 500W
Pulse Width (Internal Limitation)	100ms @ 500W (up to CW @ 50W)
Duty Cycle (Internal Limitation)	10% @ 500W (up to 100% @ 50W)
Droop & Pulse Flatness	±4% typical @ 500W for 100ms Pulse Width
Amplitude Stability vs. Temperature	±0.1% / °C max.

Specifications

Service Information and Maintenance

8

Every intervention on the device must be carried out by an authorized and qualified person. Any failure due to a non-respect of the following instructions will not be attributable to BRUKER BIOSPIN and will not be covered by the guarantee clauses.

Preventive Maintenance of the RF Module on BLA-Type Amplifiers

The RF module inside BLA's Amplifiers is equipped with a easily extractible PUSH FAN Assembly.

Fan's on assembly have a high reliability and manufacturer gives a expected live time of 70000 hours (8 years) at 25° C and 5 years at 60° C.

Replacement of the assembly could be done in the field when a misfonction of fans is detected by lightning from the OVERHEAT Status Led.

To prevent such a misfonction, a preventive maintenance could be done every 4 years.

This assembly can be ordered on the manufactory BBIO-FR by P/N:

• W1346523 «PUSH FAN ASSEMBLY 6».

Opera	ation
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8.1.1

8.1

- 1. Disconnect all cables from the front panel and the supply connector on the rear panel. Remove the amplifier from the NMR console and place it on a secure flat surface.
- 2. Unscrew and remove the coverage plate from the amplifier.
- Disconnect the 2 wires (red +32V / black GND) being on the RF module dispatch supply connectors and coming from the Push fan assembly. Also disconnect the fan status wires (white) from BLA Control board connector J18.

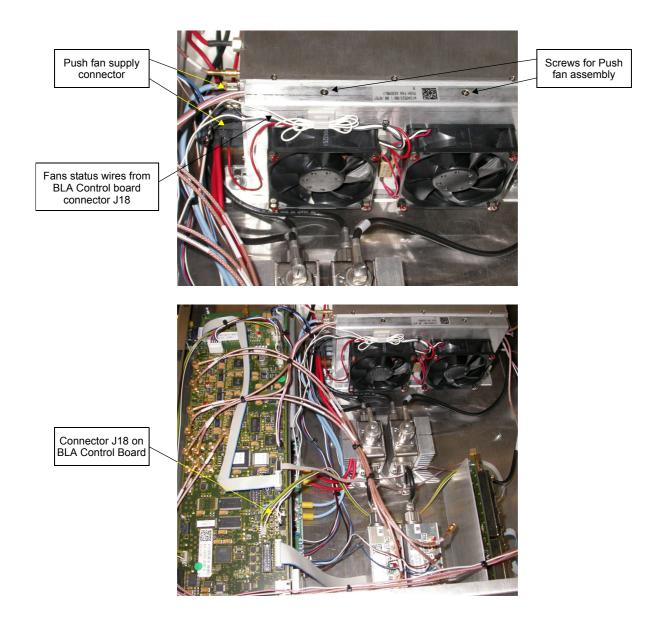


Figure 8.1. Push Fan Assembly

- 4. Unscrew the 2 screws on the top of the Push fan assembly.
- 5. Remove the Push fan assembly.
- 6. Place correctly the new fan assembly in the bottom holes of the RF module and screw it on the top.
- 7. Connect all wires (status and supply).
- 8. Connect line cord and turn on the amplifier. Note that the fans are turning and no OVERHEAT status led appears on front panel.
- 9. Put the coverage plate on the amplifier and screw it.
- 10. Put the amplifier in the NMR console, connect all cables on the front panel and the line cord on the rear panel.

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